



16CTQ...
16CTQ...S
16CTQ...-1

SCHOTTKY RECTIFIER

16 Amp

$I_{F(AV)} = 15\text{Amp}$
 $V_R = 60/ 100\text{V}$

Major Ratings and Characteristics

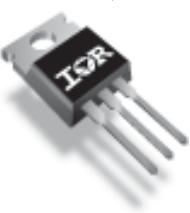
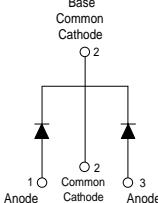
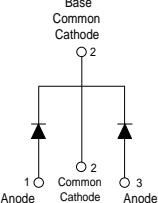
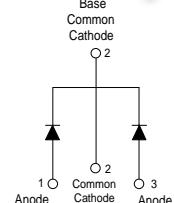
Characteristics	Values	Units
$I_{F(AV)}$ Rectangular waveform	16	A
V_{RRM}	60 / 100	V
I_{FSM} @ $t_p = 5\ \mu\text{s}$ sine	850	A
V_F @ 8Apk , $T_J = 125^\circ\text{C}$ (per leg)	0.58	V
T_J range	-55 to 175	$^\circ\text{C}$

Description/ Features

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175°C junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

- $175^\circ\text{ C } T_J$ operation
- Center tap configuration
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability

Case Styles

16CTQ...	16CTQ... S	16CTQ... -1
  TO-220	  D2PAK	  TO-262

16CTQ..., 16CTQ...S, 16CTQ...-1 Series

Voltage Ratings

Parameters	16CTQ060 16CTQ060S 16CTQ060-1	16CTQ80 16CTQ80S 16CTQ80-1	16CTQ100 16CTQ100S 16CTQ100-1
V_R Max. DC Reverse Voltage (V)	60	80	100
V_{RWM} Max. Working Peak Reverse Voltage (V)			

Absolute Maximum Ratings

Parameters	Values	Units	Conditions
$I_{F(AV)}$ Max. Average Forward Current * See Fig. 5 (Per Leg)	8	A	50% duty cycle @ $T_J = 148^\circ\text{C}$, rectangular wave form
Current * See Fig. 5 (Per Device)	16		
I_{FSM} Max. Peak One Cycle Non-Repetitive Surge Current (Per Leg) * See Fig. 7	850	A	5μs Sine or 3μs Rect. pulse
	275		Following any rated load condition and with 10ms Sine or 6ms Rect. pulse
E_{AS} Non-Repetitive Avalanche Energy (Per Leg)	7.50	mJ	$T_J = 25^\circ\text{C}$, $I_{AS} = 0.50$ Amps, $L = 60$ mH
I_{AR} Repetitive Avalanche Current (Per Leg)	0.50	A	Current decaying linearly to zero in 1 μsec Frequency limited by T_J max. $V_A = 1.5 \times V_R$ typical

Electrical Specifications

Parameters	Values	Units	Conditions
V_{FM} Max. Forward Voltage Drop (Per Leg) * See Fig. 1 (1)	0.72	V	$T_J = 25^\circ\text{C}$
	0.88	V	$T_J = 125^\circ\text{C}$
	0.58	V	
	0.69	V	
I_{RM} Max. Reverse Leakage Current (Per Leg) * See Fig. 2 (1)	0.55	mA	$T_J = 25^\circ\text{C}$
	7.0	mA	$T_J = 125^\circ\text{C}$
$V_{F(TO)}$ Threshold Voltage	0.415	V	$T_J = T_J$ max
r_t Forward Slope Resistance	11.07	mΩ	
C_T Max. Junction Capacitance(Per Leg)	500	pF	$V_R = 5V_{DC}$ (test signal range 100Khz to 1Mhz) 25°C
L_S Typical Series Inductance (Per Leg)	8.0	nH	Measured lead to lead 5mm from package body
dv/dt Max. Voltage Rate of Change (Rated V_R)	10000	V/ μs	

(1) Pulse Width < 300μs, Duty Cycle <2%

Thermal-Mechanical Specifications

Parameters	Values	Units	Conditions
T_J Max. Junction Temperature Range	-55 to 175	°C	
T_{stg} Max. Storage Temperature Range	-55 to 175	°C	
R_{thJC} Max. Thermal Resistance Junction to Case (Per Leg)	3.25	°C/W	DC operation
R_{thJC} Max. Thermal Resistance Junction to Case (Per Package)	1.63	°C/W	DC operation
R_{thCS} Typical Thermal Resistance, Case to Heatsink	0.50	°C/W	Mounting surface , smooth and greased (only for TO-220)
wt Approximate Weight	2 (0.07)	g (oz.)	
T Mounting Torque	Min.	6 (5)	Kg-cm (lbf-in)
	Max.	12 (10)	

16CTQ..., 16CTQ...S, 16CTQ...-1 Series

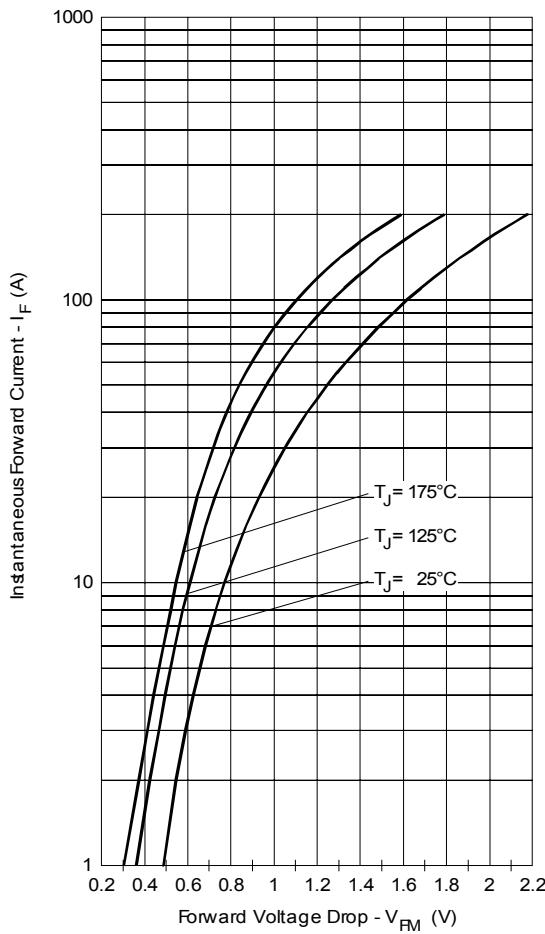


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

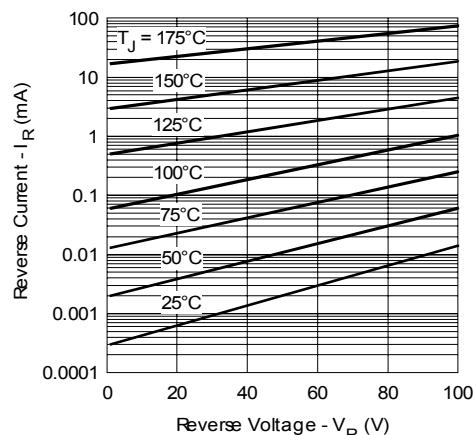


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage (Per Leg)

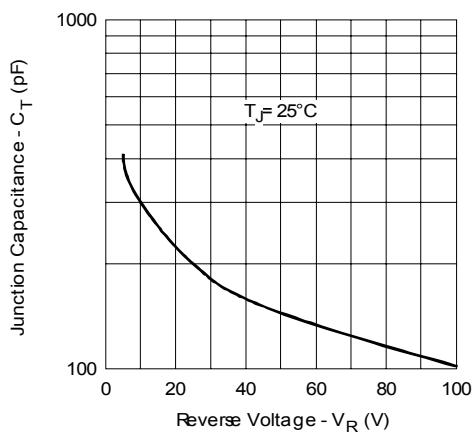


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

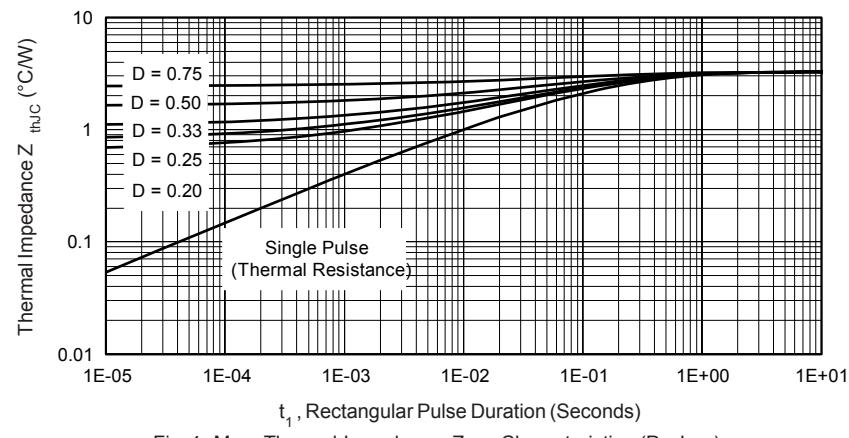


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

16CTQ..., 16CTQ...S, 16CTQ...-1 Series

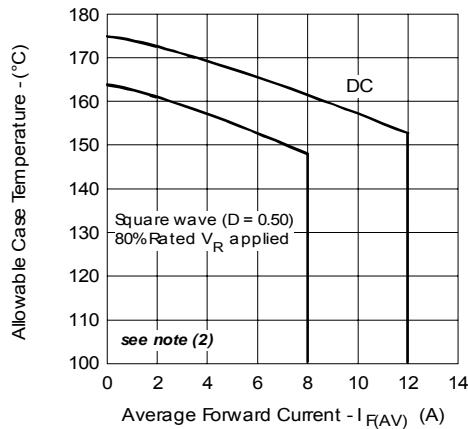


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

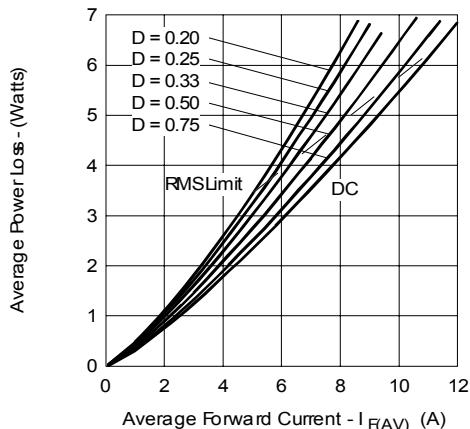


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

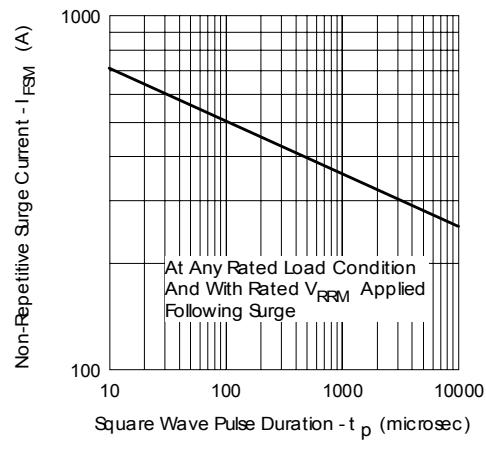


Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

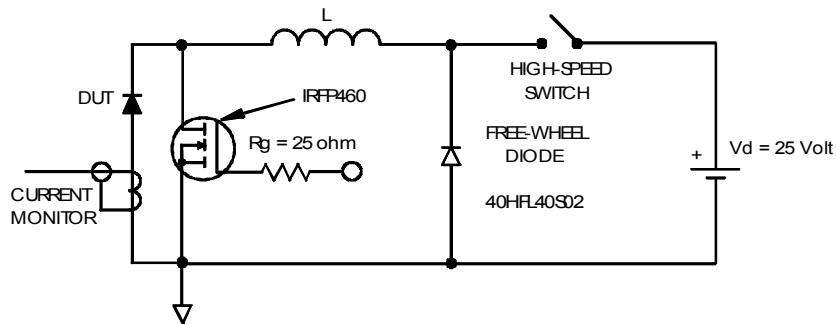
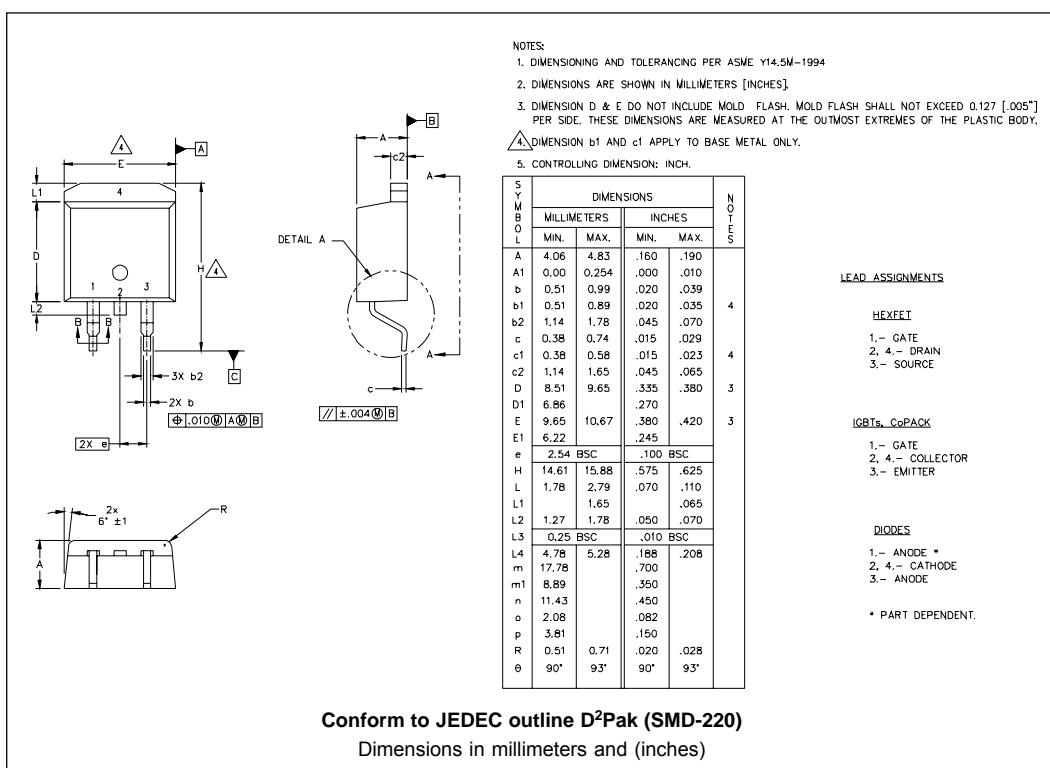
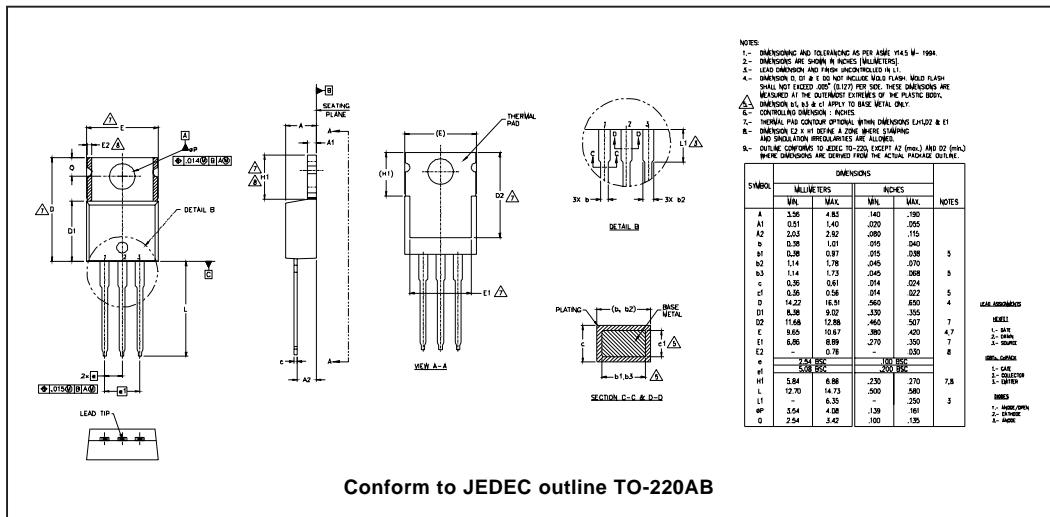


Fig. 8 - Unclamped Inductive Test Circuit

- (2) Formula used: $T_c = T_j - (P_d + P_{d,REV}) \times R_{thJC}$;
 $P_d = \text{Forward Power Loss} = I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);
 $P_{d,REV} = \text{Inverse Power Loss} = V_{R1} \times I_R (1 - D)$; $I_R @ V_{R1} = 80\% \text{ rated } V_R \text{ applied}$

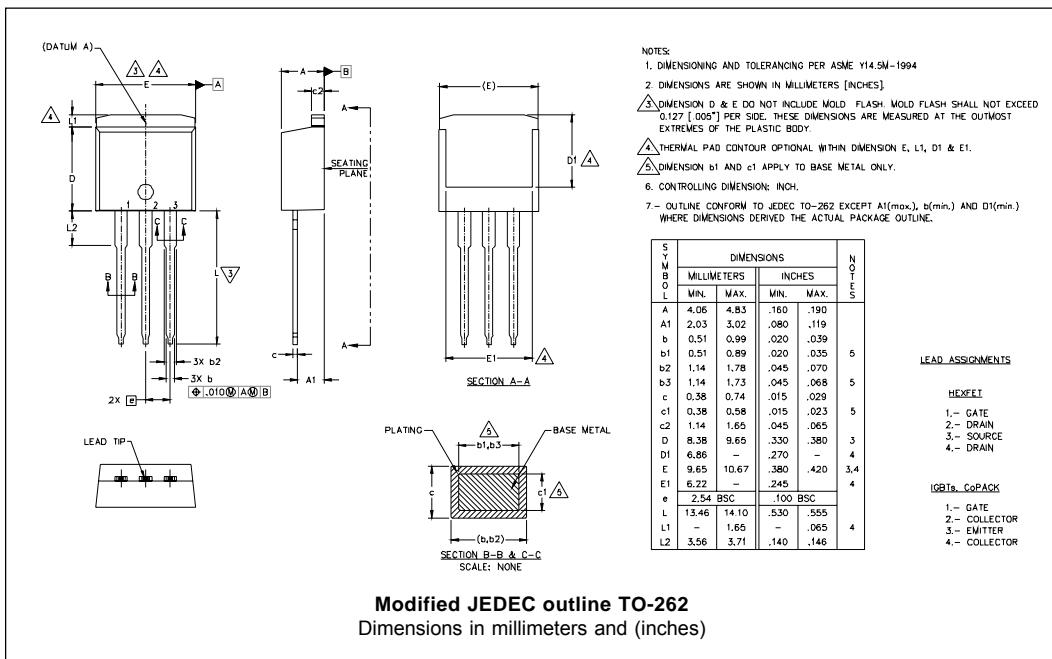
16CTQ..., 16CTQ...S, 16CTQ...-1 Series

Outline Table



16CTQ..., 16CTQ...S, 16CTQ...-1 Series

Outline Table



Tape & Reel Information

